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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/792,366	03/03/2004	Chih-Chiang Su	10113881	4262
34283 7	590 01/24/2006	EXAMINER		
~	LAW OFFICE WAY, 3RD FLOOR	VU, PHU		
	ICA, CA 90404		ART UNIT	PAPER NUMBER
			2871	
			DATE MAILED: 01/24/2006	5

Please find below and/or attached an Office communication concerning this application or proceeding.

		Application No.	Applicant(s)	<del></del>			
Office Action Summary		10/792,366	10/792,366 SU, CHIH-CHIANG				
		Examiner	Art Unit	( )			
		Phu Vu	2871				
Period fo	The MAILING DATE of this communicat or Reply	tion appears on the cover sheet	with the correspondence addre	ss			
WHIC - Exter after - If NC - Failu Any r	ORTENED STATUTORY PERIOD FOR CHEVER IS LONGER, FROM THE MAIL asions of time may be available under the provisions of 3 SIX (6) MONTHS from the mailing date of this communic period for reply is specified above, the maximum statutor to reply within the set or extended period for reply will, eply received by the Office later than three months after the patent term adjustment. See 37 CFR 1.704(b).	ING DATE OF THIS COMMU 7 CFR 1.136(a). In no event, however, may ation. ry period will apply and will expire SIX (6) N by statute, cause the application to become	NICATION.  y a reply be timely filed  MONTHS from the mailing date of this comm  a ABANDONED (35 U.S.C. § 133).	·			
Status							
1)	Responsive to communication(s) filed of	on .	•				
2a)□	· · · · · · · · · · · · · · · · · · ·	☐ This action is non-final.					
3)	Since this application is in condition for		atters, prosecution as to the m	erits is			
-,	closed in accordance with the practice	•	• •				
Dispositi	on of Claims						
4)	Claim(s) 1-16 is/are pending in the appl	lication.					
•	4a) Of the above claim(s) is/are v						
	Claim(s) is/are allowed.			•			
′=	Claim(s) <u>1-16</u> is/are rejected.			•			
7)	Claim(s) is/are objected to.						
8) 🗌	Claim(s) are subject to restriction	n and/or election requirement.					
Applicati	ion Papers						
	The specification is objected to by the E	vaminer					
			to by the Examiner				
10)	10) The drawing(s) filed on is/are: a) accepted or b) objected to by the Examiner.  Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a).						
	Replacement drawing sheet(s) including the			1 121(d)			
11)	The oath or declaration is objected to by	·	= 1 /				
, —	-						
	ınder 35 U.S.C. § 119						
•	Acknowledgment is made of a claim for ⊠ All b) ☐ Some * c) ☐ None of:  1. ☐ Certified copies of the priority do	cuments have been received.					
	<ul><li>2. Certified copies of the priority doe</li><li>3. Copies of the certified copies of the application from the International</li></ul>	he priority documents have be	• •	nge			
* 5	See the attached detailed Office action for		not received.				
Attachmen	t(s)	. •					
1) Notic	e of References Cited (PTO-892)		ew Summary (PTO-413)				
2) Notice (3) Inform	re of Draftsperson's Patent Drawing Review (PTO- mation Disclosure Statement(s) (PTO-1449 or PTO r No(s)/Mail Date	-948) Paper I	No(s)/Mail Date of Informal Patent Application (PTO-15	52)			
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#### **DETAILED ACTION**

## Claim Rejections - 35 USC § 102

The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102 that form the basis for the rejections under this section made in this Office action:

A person shall be entitled to a patent unless -

(b) the invention was patented or described in a printed publication in this or a foreign country or in public use or on sale in this country, more than one year prior to the date of application for patent in the United

Claim 1-6, 9-10, and 13-14 are rejected under 35 U.S.C. 102(b) as being anticipated by Huynh US 6317172.

Regarding claim 1, Huynh teaches an electronic device comprising: a parinted circuit board including a ground layer (fig. 1 element M), a liquid crystal display module (4) disposed on the printed circuit board, a surrounding portion (2), an anti-ESD wire (9) disposed on the surrounding portion, wires of the display module (5-I) are schemed between the anti ESD wire and the central portion, and the first contact is coupled to the anti-ESD module (7) so that ESD in the LCD module is ground via the anti-ESD wire and the first contact, and a controller disposed on the PCB and coupled to the LCD display module for resetting at a predetermined interval

Regarding claim 2, the reference shows a first wire connecting the first contact (7) to the ground (MF).

Regarding claim 3, the reference shows the LCD module includes a plurality of second contacts and the first contact is located outside of the second contacts (5-I).

Regarding claim 4, the reference shows a second wire connecting one of the second contacts and the controller (see wire 5-i).

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Regarding claim 5, the reference shows the central portion being the LCDM (4).

**Regarding claim 6,** the reference shows the surrounding portion of the LCDM is a circuit layout region of the LCDM (see fig. 1).

Regarding claims 9 and 13, Huynh teaches an ESD method/machine readable storage medium for a n electronic device comprising a printed circuit board including a ground layer (fig. 1 element M), a liquid crystal display module (4) disposed on the printed circuit board, a surrounding portion (2), an anti-ESD wire (9) disposed on the surrounding portion, wires of the display module (5-I) are schemed between the anti-ESD wire and the central portion, and the first contact is coupled to the anti-ESD module (7) so that ESD in the LCD module is ground via the anti-ESD wire and the first contact, and a controller disposed on the PCB and coupled to the LCD display module at a predetermined interval. The limitation of "predetermined" interval does not impose any sort of fixed interval therefore the predetermined interval is considered whenever Huynh's device is reset.

Regarding claims 10 and 14, the limitation of returning a level of the liquid crystal display is met because the "predetermined level" is an arbitrary amount therefore the predetermined level is considered to be the reset state of the display of Huynh.

## Claim Rejections - 35 USC § 103

The following is a quotation of 35 U.S.C. 103(a) which forms the basis for all obviousness rejections set forth in this Office action:

(a) A patent may not be obtained though the invention is not identically disclosed or described as set forth in section 102 of this title, if the differences between the subject matter sought to be patented and the prior art are such that the subject matter as a whole would have been obvious at the time the

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invention was made to a person having ordinary skill in the art to which said subject matter pertains. Patentability shall not be negatived by the manner in which the invention was made.

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Claims 7, 11, and 15 are rejected under 35 U.S.C. 103(a) as being unpatentable over Huynh in view of Kim US 5517342.

Regarding claims 7, 11 and 15, Huynh teaches all the limitations of claims 7, 11, and 15 except an ITO wiring. Kim teaches ITO as a replacement for traditional aluminum wiring for display applications such as LCDs because of its transparency (column 5 lines 10-15). Therefore, at the time of the invention, it would have been obvious to one of ordinary skill in the art to apply ITO wiring in display application because of its transparency.

Claims 8, 12, and 16 are rejected under 35 U.S.C. 103(a) as being unpatentable over Huynh in view of Yuuki et al US 6181071.

Regarding claim 8 and 12 and 16, Park and Cha disclose all the limitations of the claim except wire widths of .15 mm to .35 mm. Yuuki discloses a wire with of .1 mm - 1 mm to serve as a shield against electromagnetic waves (See column 8 lines 22-30). The MPEP section 2144.01 states In the case where the claimed ranges "overlap or lie inside ranges disclosed by the prior art" a prima facie case of obviousness exists. In re Wedheim, 541 F.2d 257, 191 USPQ 90 (CCPA 1976). Therefore, at the time of the invention, it would have been obvious to use wire widths of .15 to .35 mm to serve as electromagnetic shielding.

#### Conclusion

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Any inquiry concerning this communication or earlier communications from the

examiner should be directed to Phu Vu whose telephone number is (571)-272-1562.

The examiner can normally be reached on 8AM-5PM M-F.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's

supervisor, Robert Kim can be reached on (571)-272-2293. The fax phone number for

the organization where this application or proceeding is assigned is 571-273-8300.

Information regarding the status of an application may be obtained from the

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you have questions on access to the Private PAIR system, contact the Electronic

Business Center (EBC) at 866-217-9197 (toll-free).

Phu Vu Examiner

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ANDREW SCHECHTER PRIMARY EXAMINER